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APPLICATION NO.	PLICATION NO. FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/612,383 07/01/2003		Ka Shing Kenny Kwan	P/4076-54	3682	
2352	7590	08/02/2006		EXAM	INER
	OSTROLENK FABER GERB & SOFFEN 1180 AVENUE OF THE AMERICAS			JOHNSON, JONATHAN J	
NEW YORK		0368403		ART UNIT	PAPER NUMBER
•			1725		

DATE MAILED: 08/02/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)
·	10/612,383	KWAN ET AL.
Office Action Summary	Examiner	Art Unit
	Jonathan Johnson	1725
The MAILING DATE of this communication Period for Reply	appears on the cover sheet wi	th the correspondence address
A SHORTENED STATUTORY PERIOD FOR RE WHICHEVER IS LONGER, FROM THE MAILING  - Extensions of time may be available under the provisions of 37 CFI after SIX (6) MONTHS from the mailing date of this communication  - If NO period for reply is specified above, the maximum statutory period for reply within the set or extended period for reply will, by stany reply received by the Office later than three months after the meanned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNIC R 1.136(a). In no event, however, may a re- riod will apply and will expire SIX (6) MON atute, cause the application to become AB	CATION.  apply be timely filed  THS from the mailing date of this communication.  ANDONED (35 U.S.C. § 133).
Status		
Responsive to communication(s) filed on 6     This action is <b>FINAL</b> . 2b) ☐ 3     Since this application is in condition for allo closed in accordance with the practice under	This action is non-final.  wance except for formal matte	
Disposition of Claims		
<ul> <li>4)  Claim(s) 1-25 is/are pending in the applicate 4a) Of the above claim(s) 14-25 is/are without 5)  Claim(s) is/are allowed.</li> <li>6)  Claim(s) 1-13 is/are rejected.</li> <li>7)  Claim(s) is/are objected to.</li> <li>8)  Claim(s) 1-25 are subject to restriction and/</li> </ul>	drawn from consideration.	
Application Papers		
9) The specification is objected to by the Exam	niner.	
10)☐ The drawing(s) filed on is/are: a)☐ a	accepted or b)☐ objected to t	by the Examiner.
Applicant may not request that any objection to		
Replacement drawing sheet(s) including the cor 11) The oath or declaration is objected to by the	·	
Priority under 35 U.S.C. § 119		
12) ☐ Acknowledgment is made of a claim for fore a) ☐ All b) ☐ Some * c) ☐ None of:		119(a)-(d) or (f).
1. Certified copies of the priority docum		polication No
<ul><li>2. Certified copies of the priority docum</li><li>3. Copies of the certified copies of the priority docum</li></ul>		•
application from the International But	•	. Coc. Tod III and Hadional Olage
* See the attached detailed Office action for a	• • • • • • • • • • • • • • • • • • • •	received.
Attachment(s)		
1) Notice of References Cited (RTO 902)	4) Intondous	Jumman (PTO 413)

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2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 6-19-06.

4)	Interview Summary (PTO-413)				
	Paper No(s)/Mail Date.				

5) Notice of Informal Patent Application (PTO-152)

6)		Other:
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#### DETAILED ACTION

# Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 2, and 4-13 are rejected under 35 U.S.C. 102(b) as being anticipated by Hisataka (JP 01-296636). Histaka teaches a bump forming device (figure 7, item 7); a chamber system adapted to house the semiconductor devices (Figure 1, item 10); a gas supply for supplying an inert gas into the chamber system (figure 1, item 31); and a support table for supporting the semiconductor devices during bumping, the chamber system having an opening (figure 1, item 10, where the opening extends from item 10 to item 14); a bumping site located outside of the chamber system (figure 1, item 7, where features in figure 6, item 20 and 26a and portions of item 7 are located outside the chamber system), said support table being operative to move the semiconductor devices from a bumping site into the chamber system after bumping (figure 1, item 3); at the bumping site the support table being receivable into the chamber system through the opening (figure 1, item 3); including an oxidation reduction device for introducing a supply of inert gas to the bumping site (figure 1, item 31); wherein the bumping site is adjacent to an opening of the chamber system (figure 3, item 17); including a shroud positioned around the bumping site for covering one or more bumped semiconductor devices (figure 1, item 15); wherein the shroud includes nozzles for introducing a supply of inert gas onto the semiconductor Application/Control Number: 10/612,383

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devices (figure 1, item 31); wherein the chamber system comprises an outer chamber, and an inner chamber that is houseable within the outer chamber (figure 6, items 11 and 2); wherein the inner chamber is removable from the outer chamber (figure 1, items 11 and 2); wherein the removable inner chamber comprises at least a portion of the support table and an inner chamber cover for forming an enclosure around bumped semiconductor devices (figure 1, item 11); including gas tubings that are detachably connectable to the inner chamber for bringing an inert gas into the inner chamber from an external source (figure 1, item 31); wherein the support table is coupled to a positioning device that is operative to move the support table along a first axis (figure 3, x axis); wherein the chamber system is coupled to a positioning device that is operative to move the chamber system along a second axis perpendicular to the first axis (figure 3, y axis); wherein the bump forming device is an ultrasonic wire bonder (abstract); wherein the semiconductor devices are comprised in a semiconductor wafer and the support table capable of supporting a wafer on the table (figure 1, item 3).

# Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 1 is rejected under 35 U.S.C. 103(a) as being unpatentable over Okuyama (DE 3429375) in view of Hisataka (JP 01-296636). Okuyama teaches a chamber system to house the semiconductor device having a access opening (figure 4, item 2); a bump forming device

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adjacent to the opening and adjacent to the acces opening (figure 4, items 1 and 2) and a support table for supporting the semiconductor device (figure 4, item 13). Histaka teaches a gas supply for supplying an inert gas into the chamber system (figure 1, item 31). It would have been obvious to one of ordinary skill in the art at the time of the invention to modify the apparatus of Okuvama to utilize the inert gas of Histaka in order to prevent oxidation (see Histaka abstract).

### Response to Arguments

Applicant's arguments with respect to claims 1-13 have been considered but are moot in view of the new ground(s) of rejection. In particular the features in figure 6, item 20 and 26a and portions of item 7 show portions of the bump forming device located outside the chamber system.

#### Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event,

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however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jonathan Johnson whose telephone number is 571-272-1177. The examiner can normally be reached on M-Th 7:30 AM-5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Pat Ryan can be reached on 571-272-1292. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-21/7-9197 (toll-free).

Jonathan Johnson Primary Examiner Art Unit 1725